



MATLAB®
& SIMULINK®

Design and Prototyping for FPGA/SoCs with MATLAB/Simulink

21 Aug 2024 (Wednesday) | 04:00 pm to 06:00 pm IST
Institute of Engineering and Management, Kolkata

Presenter:

This discussion will cover the following topics:

Highlights

- Challenges with Traditional Implementation Flow
- Model Based Design for Implementation
- Generate VHDL/Verilog HDL Code Generation
- FPGAs/SoCs for Signal Processing and Control
- Adv. Topics: Optimizing Digital Architectures and Generation HDL



Dr. Anand Mukhopadhyay
Senior Engineer, Education Team
MathWorks India Private Limited

Register at <https://tinyurl.com/IEMKoIVLSI>
Check the registration form for required setup

